

	L #	Hits	Search Text	DBs
1	L1	3383	(361/767,768,769,770,7 71,777,778,779,782,783 ,784,785,790,791,803).c cls.	USP AT; US-P GPU B
2	L2	3868	(257/691,698,700,723,7 24,728,786,664).ccls.	USP AT; US-P GPU B
3	L3	1462	(333/33,247).ccls.	USP AT; US-P GPU B
4	L4	1463	324/765.ccls.	USP AT; US-P GPU B
5	L5	340	(363/144,146,147).ccls.	USP AT; US-P GPU B
6	L6	9930	1 2 3 4 5	USP AT; US-P GPU B
7	L7	2438	6 and (convert\$3 invert\$3 regulat\$3 rectif\$ conversion inversion)	USP AT; US-P GPU B

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
8	<b>L9</b>	<b>79</b>	<b>7 and ((power adj module\$1) (power adj pod\$1))</b>	<b>USP AT; US-P GPU B</b>
9	<b>L8</b>	<b>106</b>	<b>7 and adapter\$1</b>	<b>USP AT; US-P GPU B</b>
10	<b>L10</b>	<b>77</b>	<b>9 not 8</b>	<b>USP AT; US-P GPU B</b>
11	<b>L11</b>	<b>64</b>	<b>7 and interposer\$1</b>	<b>USP AT; US-P GPU B</b>
12	<b>L12</b>	<b>57</b>	<b>11 not (8 10)</b>	<b>USP AT; US-P GPU B</b>
13	<b>L13</b>	<b>42</b>	<b>7 and overvoltage\$1</b>	<b>USP AT; US-P GPU B</b>
14	<b>L14</b>	<b>3</b>	<b>(8 10 12) and 13</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>15</b>	<b>L15</b>	<b>39</b>	<b>13 not 14</b>	<b>USP AT; US-P GPU B</b>
<b>16</b>	<b>L16</b>	<b>0</b>	<b>361/91.ccIs.</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L17</b>	<b>284</b>	<b>(361/91.1).ccIs.</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L18</b>	<b>156</b>	<b>17 and (convert\$3 invert\$3 regulat\$3 rectif\$ conversion inversion)</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L19</b>	<b>139</b>	<b>18 and power</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L20</b>	<b>139</b>	<b>19 not (8 10 12)</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L21</b>	<b>8</b>	<b>17 and (adapter\$1 interposer\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>22</b>	<b>L22</b>	<b>16</b>	<b>18 not (19 21)</b>	<b>USP AT; US-P GPU B</b>
<b>23</b>	<b>L23</b>	<b>126</b>	<b>17 not (19 21 22)</b>	<b>USP AT; US-P GPU B</b>